

Notice of References Cited

Application/Control No.

09/905,157

Applicant(s)/Patent Under

Reexamination

LEWIS ET AL.

Examiner

Erik Kielin

Art Unit

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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

(Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)

U	Wolf, et al. Silicon Processing for the VLSI Era, Vol. 1-Process Technology, Lattice Press: Sunset Beach CA, 1986, p. 5.
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